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Term:	16 and 125				
Display:	20	Documents in Display Format:		Starting with Number	1
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result set

DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L26</u>	16 and 125	16	<u>L26</u>
<u>L25</u>	19 and 124	27	<u>L25</u>
<u>L24</u>	115 or 116 or 117 or 118 or 120 or 121 or 122	2040	<u>L24</u>
<u>L23</u>	(115-122)	9531129	<u>L23</u>
<u>L22</u>	matsubara-ken\$.in.	868	<u>L22</u>
<u>L21</u>	fujisawa-tomoyuki\$.in.	49	<u>L21</u>
<u>L20</u>	tamura-takayuki\$.in.	292	<u>L20</u>
<u>L19</u>	tamular-takayuki\$.in.	0	<u>L19</u>
<u>L18</u>	nozoe-atsushi\$.in.	102	<u>L18</u>
<u>L17</u>	horii-takashi\$.in.	168	<u>L17</u>
<u>L16</u>	YOSHIDA-KEIICHI\$.in.	641	<u>L16</u>
<u>L15</u>	TAKASE-YOSHINORI\$.in.	36	<u>L15</u>
<u>L14</u>	12 and L13	11	<u>L14</u>
<u>L13</u>	19 and L12	137	<u>L13</u>
<u>L12</u>	16 near4 (17 or 13)	4266	<u>L12</u>
<u>L11</u>	12 and L10	80	<u>L11</u>

<u>L10</u>	18 and 19	595	<u>L10</u>
<u>L9</u>	14 with 13 with 17	1198	<u>L9</u>
<u>L8</u>	15 with (17 or 13)	66843	<u>L8</u>
<u>L7</u>	buffer or cache.	1027928	<u>L7</u>
<u>L6</u>	15 near2 (access\$ or control\$)	194294	<u>L6</u>
<u>L5</u>	external or outside	3851251	<u>L5</u>
<u>L4</u>	bank or block	3484395	<u>L4</u>
<u>L3</u>	(nonvolatile or flash) adj2 (memory or eeprom)	200782	<u>L3</u>
<u>L2</u>	711/103.ccls.	2192	<u>L2</u>
<u>L1</u>	510150.ap.	8	<u>L1</u>

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Results for "((((((nonvolatile <or> flash) <and> memory)<in>metadata))<and>(bank <and>..."

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» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. **A 34 Mb 3.3 V serial flash EEPROM for solid-state disk applications**
 Cernea, R.; Lee, D.J.; Mofidi, M.; Chang, E.Y.; Wy-Yi Chien; Goh, L.; Yupin Fong; Yuan, J.H.; Samachisa, G.; Guterman, D.C.; Mehrotra, S.; Sato, K.; Onishi, H.; Ueda, K.; Noro, F.; Miyamoto, K.; Morita, M.; Umeda, K.; Kubo, K.;
Solid-State Circuits Conference, 1995. Digest of Technical Papers. 42nd ISSCC, 1995 IEEE International
 15-17 Feb. 1995 Page(s):126 - 127, 350
 Digital Object Identifier 10.1109/ISSCC.1995.535459
[AbstractPlus](#) | [Full Text: PDF\(1112 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 2. **Flash memory electromagnetic compatibility**
 Fogle, A.D.; Don Darling; Blish, R.C., II; Daszko, G.;
Device and Materials Reliability, IEEE Transactions on
 Volume 6, Issue 2, June 2006 Page(s):266 - 276
 Digital Object Identifier 10.1109/TDMR.2006.876573
[AbstractPlus](#) | [Full Text: PDF\(1624 KB\)](#) IEEE JNL
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Results

#1	(((nonvolatile <or> flash) <and> memory)<in>metadata)	2810
#2	(((nonvolatile <or> flash) <and> memory)<in>metadata)<AND>(bank <and> buffer<in>metadata))	6
#3	((((nonvolatile <or> flash) <and> memory)<in>metadata)<and>(bank <and> buffer<in>metadata)))<AND>((external <or> outside) <and> (control <or> access)<in>metadata))	2



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